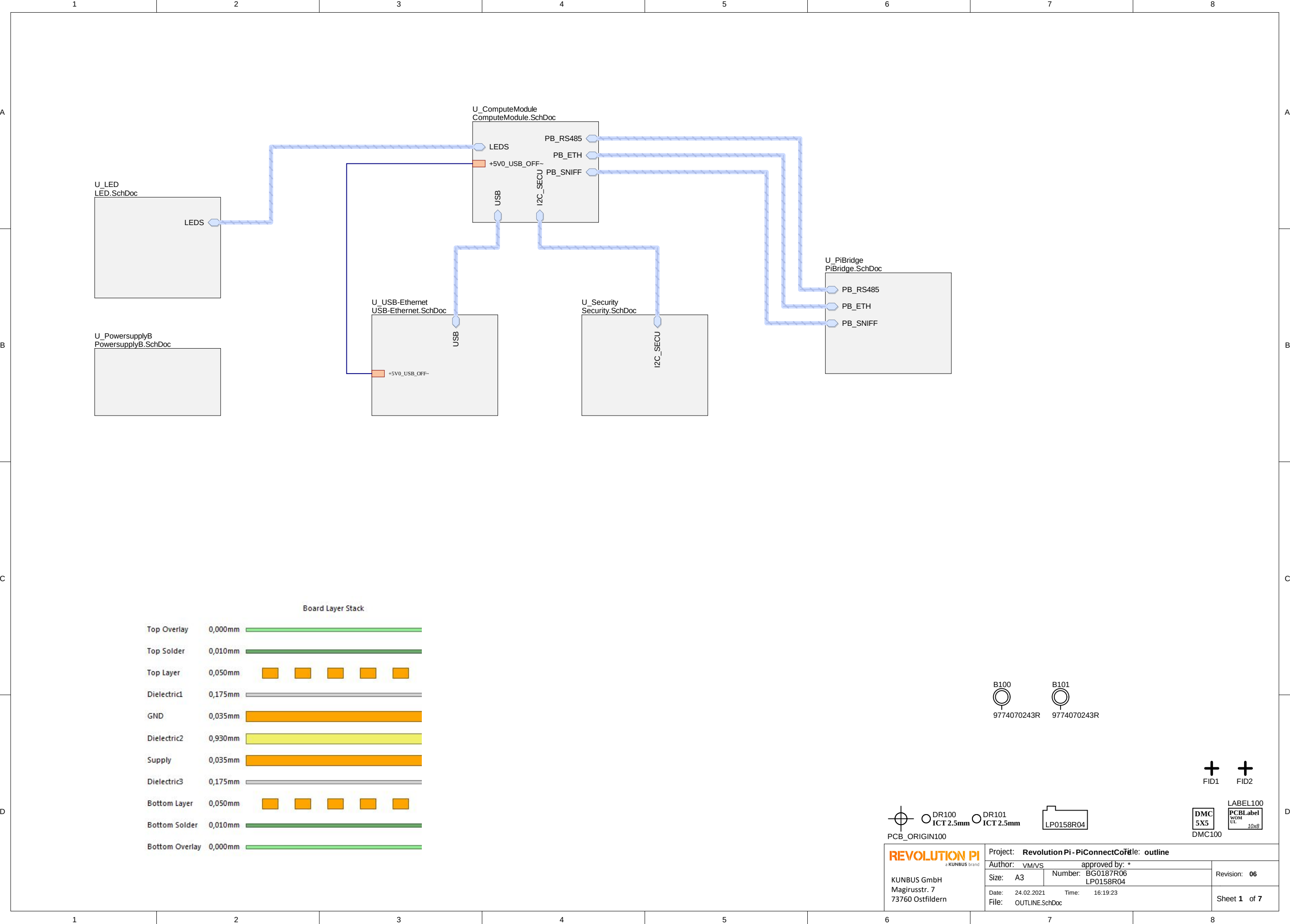




Board Layer Stack		
Top Overlay	0,000mm	
Top Solder	0,010mm	
Top Layer	0,050mm	
Dielectric1	0,175mm	
GND	0,035mm	
Dielectric2	0,930mm	
Supply	0,035mm	
Dielectric3	0,175mm	
Bottom Layer	0,050mm	
Bottom Solder	0,010mm	
Bottom Overlay	0,000mm	

B100
9774070243R

B101
9774070243R

+ FID1

+ FID2



KUNBUS GmbH
Magirusstr. 7
73760 Ostfildern

Project: **Revolution Pi - PiConnectCo**
Author: **vm/vs**
Size: **A3**
Date: **24.02.2021**
File: **OUTLINE.SchDoc**

approved by: *****
Number: **BG0187R06
LP0158R04**
Time: **16:19:23**

Revision: **06**
Sheet **1** of **7**

PCB_ORIGIN100

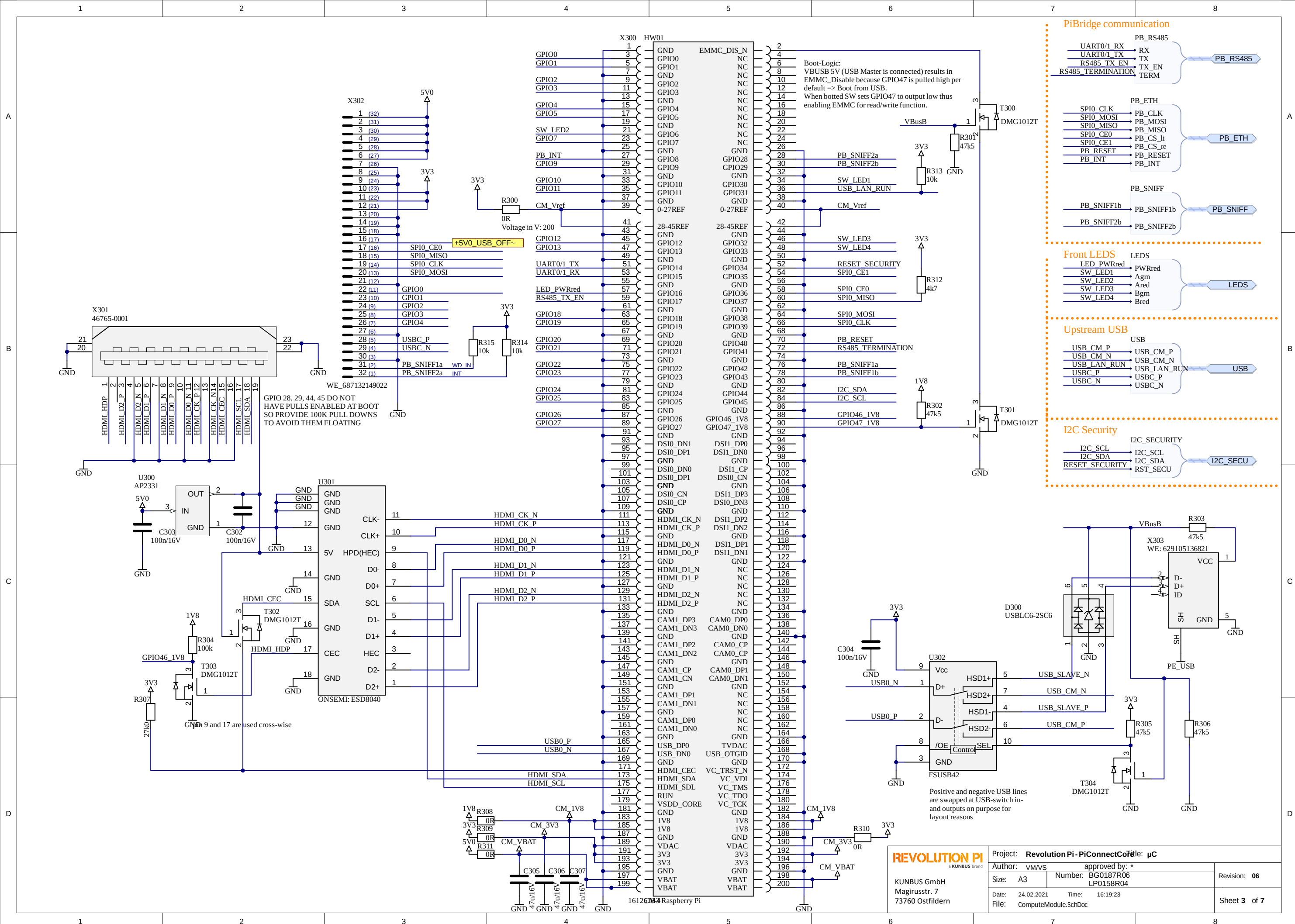
DR100
ICT 2.5mm

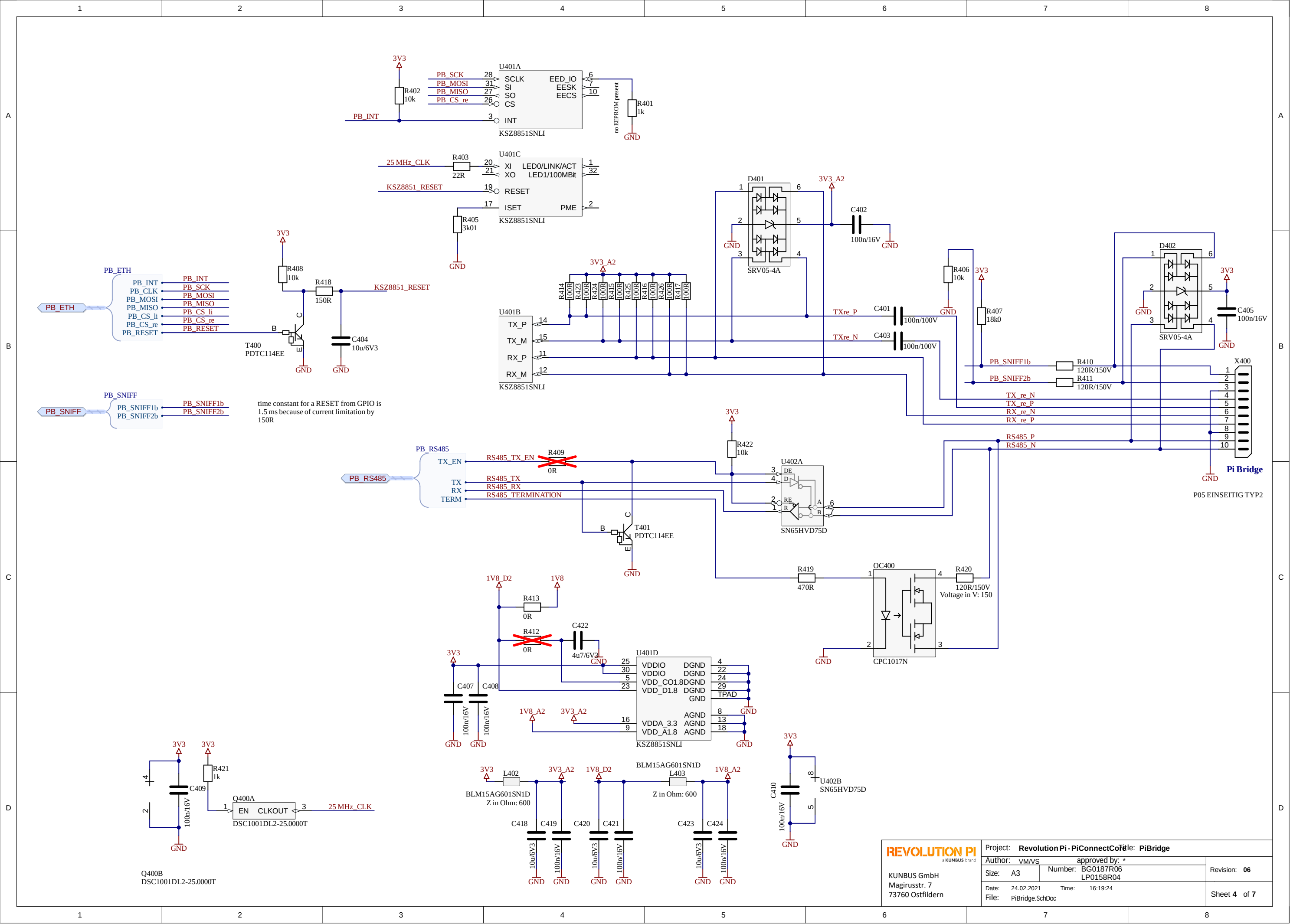
DR101
ICT 2.5mm

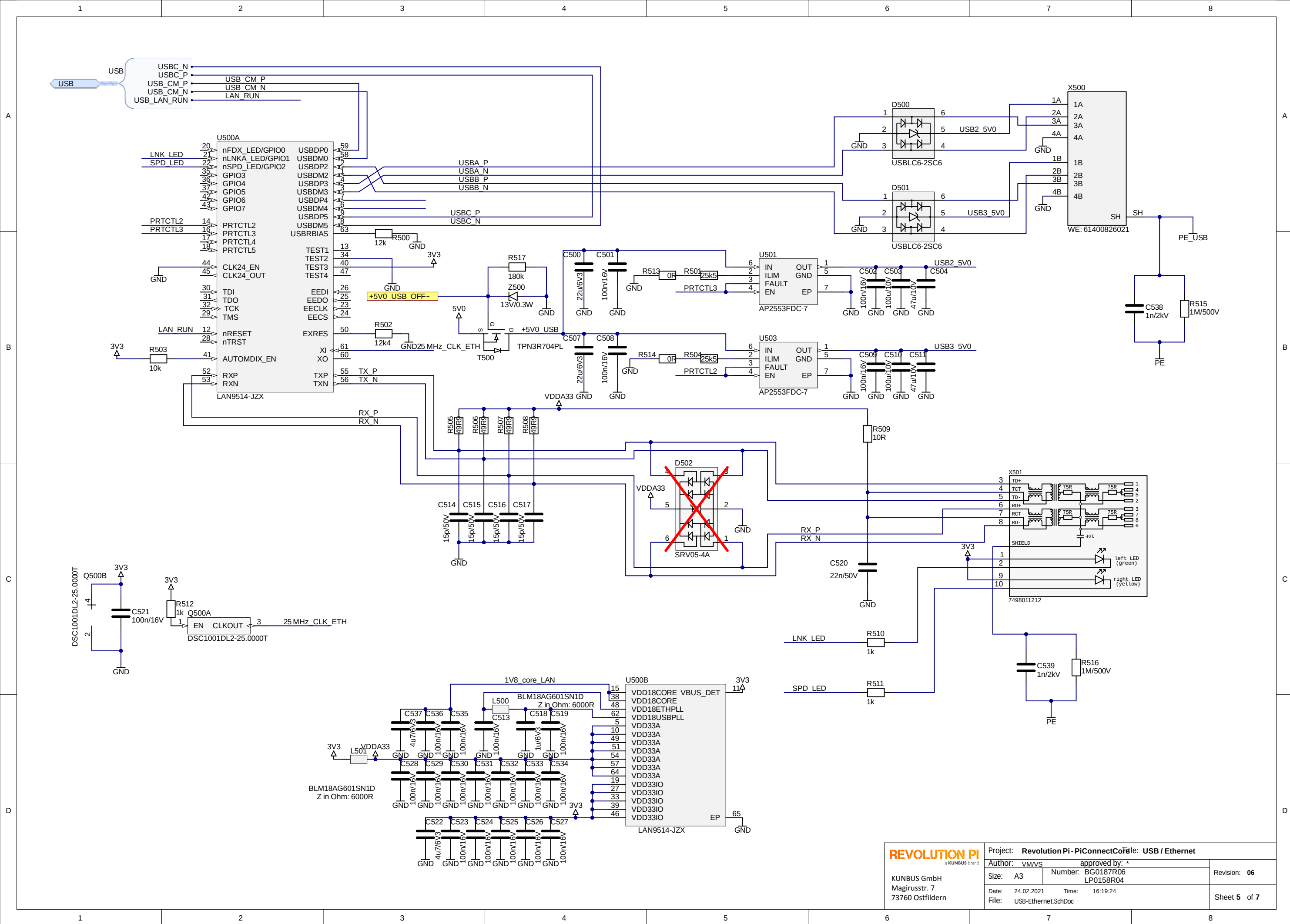
LP0158R04

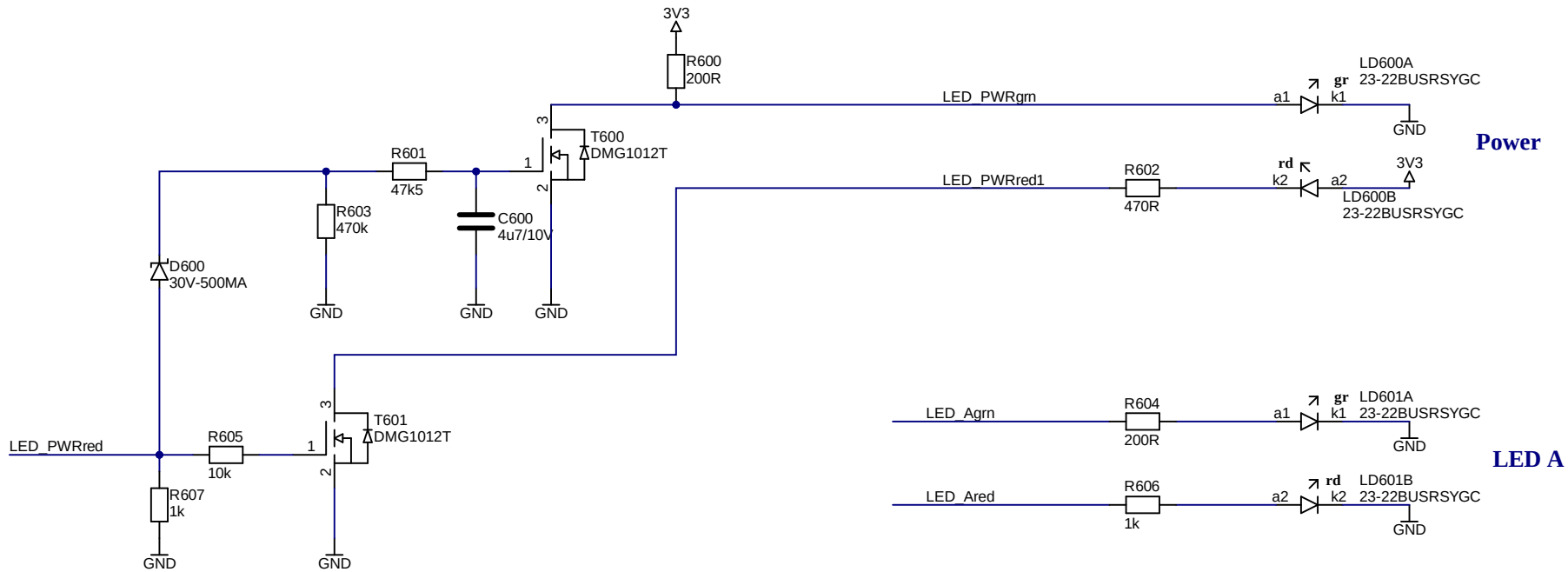
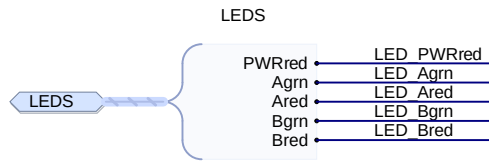
DMC 5X5
DMC100

LABEL100
PCBLabel
WOM
UL 10x8









Green Power LED function:

When system is switched on capacity draws gate of green LED to GND.
Thus FET is off and LED_PWgrn is high and green LED is on.

During startup LED_PWred is high impedance pulled up by Broadcom
and pulled down by 1k.
Thus FET is off and LED_PWgrn is high and green LED is on.

After booting Broadcom changes LED_PWred to output low.
Thus FET is off and LED_PWgrn is high and green LED is on.

On severe error Broadcom sets LED_PWred to output high.
After charging Capacitor to 1.5 V FET will turn on (max. 1 s delay).
Thus LED_PWgrn is low and green LED is off.
This mode pulls $2 \times 3.3V/1k = 6.6$ mA current.

On fault condition Braodcom sets LED_PWred to 1Hz high / low
oscillation.
After charging Capacitor to 1.5 V (during high phase) FET will turn on.
Discharge during low phase of LED_PWred is much slower and
capacitor is not discharged.
Thus LED_PWgrn keeps low and green LED stays off.
LED turns on again when LED_PWred is low for more than 3 seconds.

