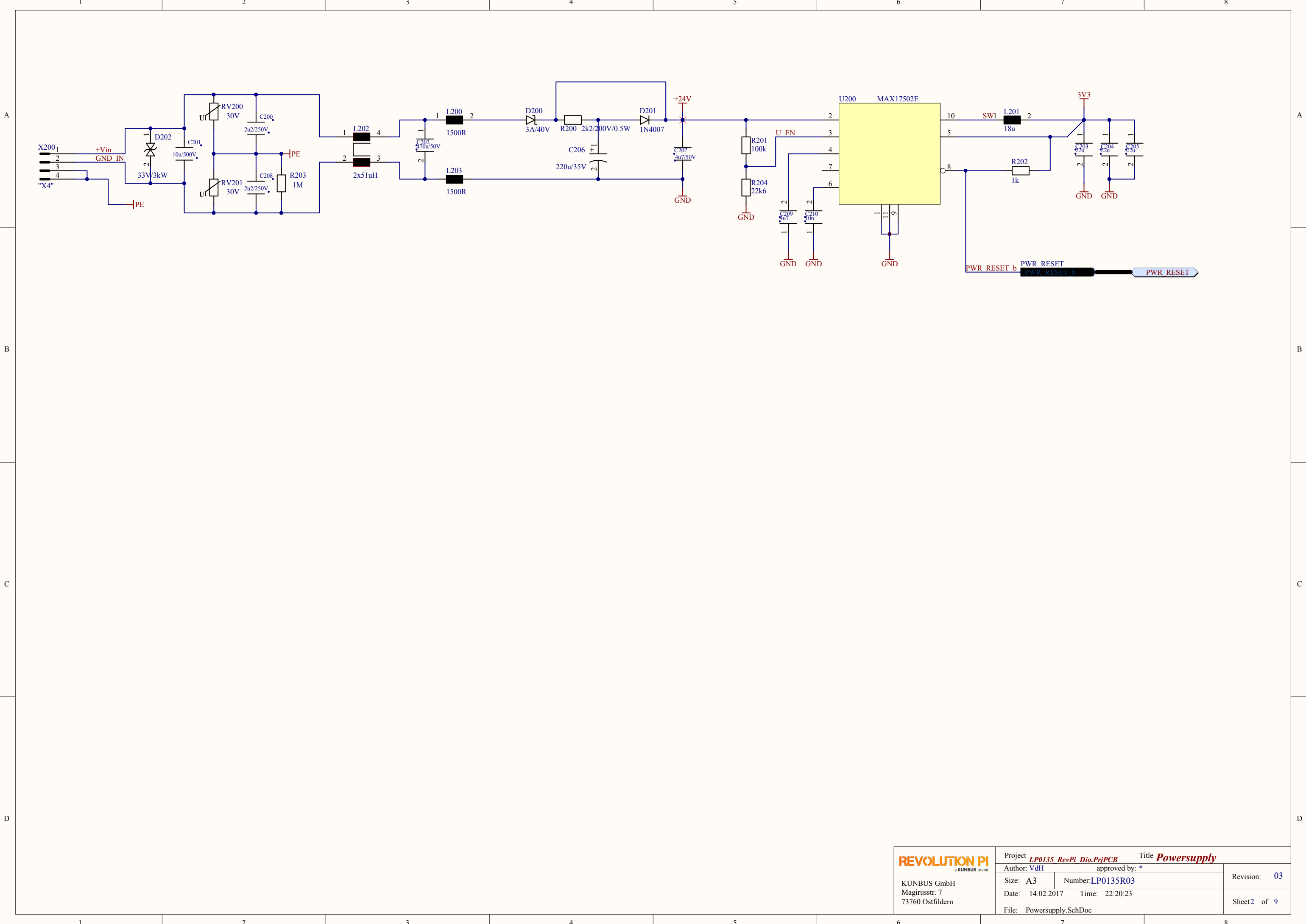
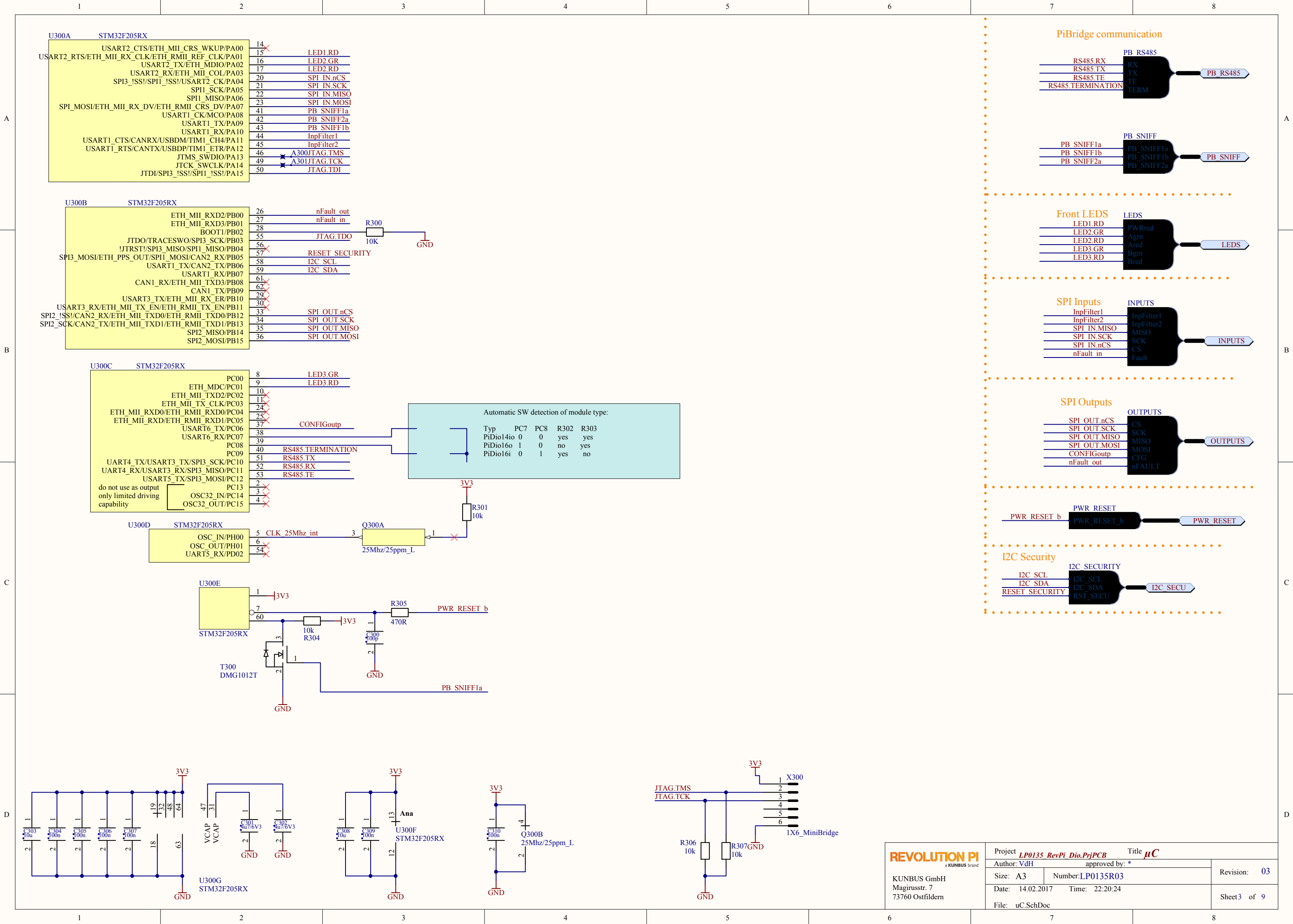
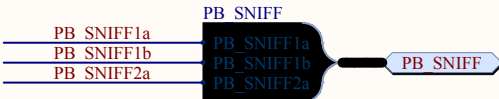
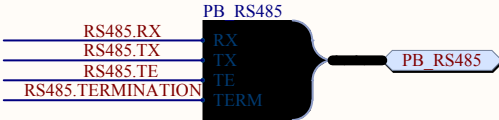


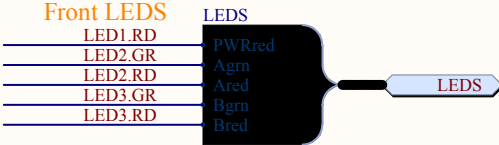




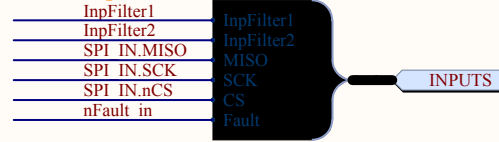
PiBridge communication



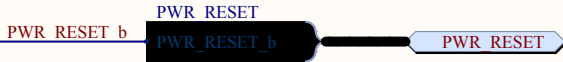
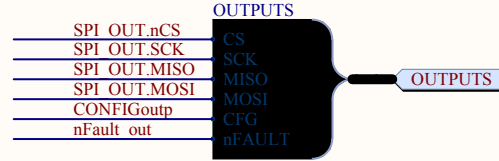
Front LEDS



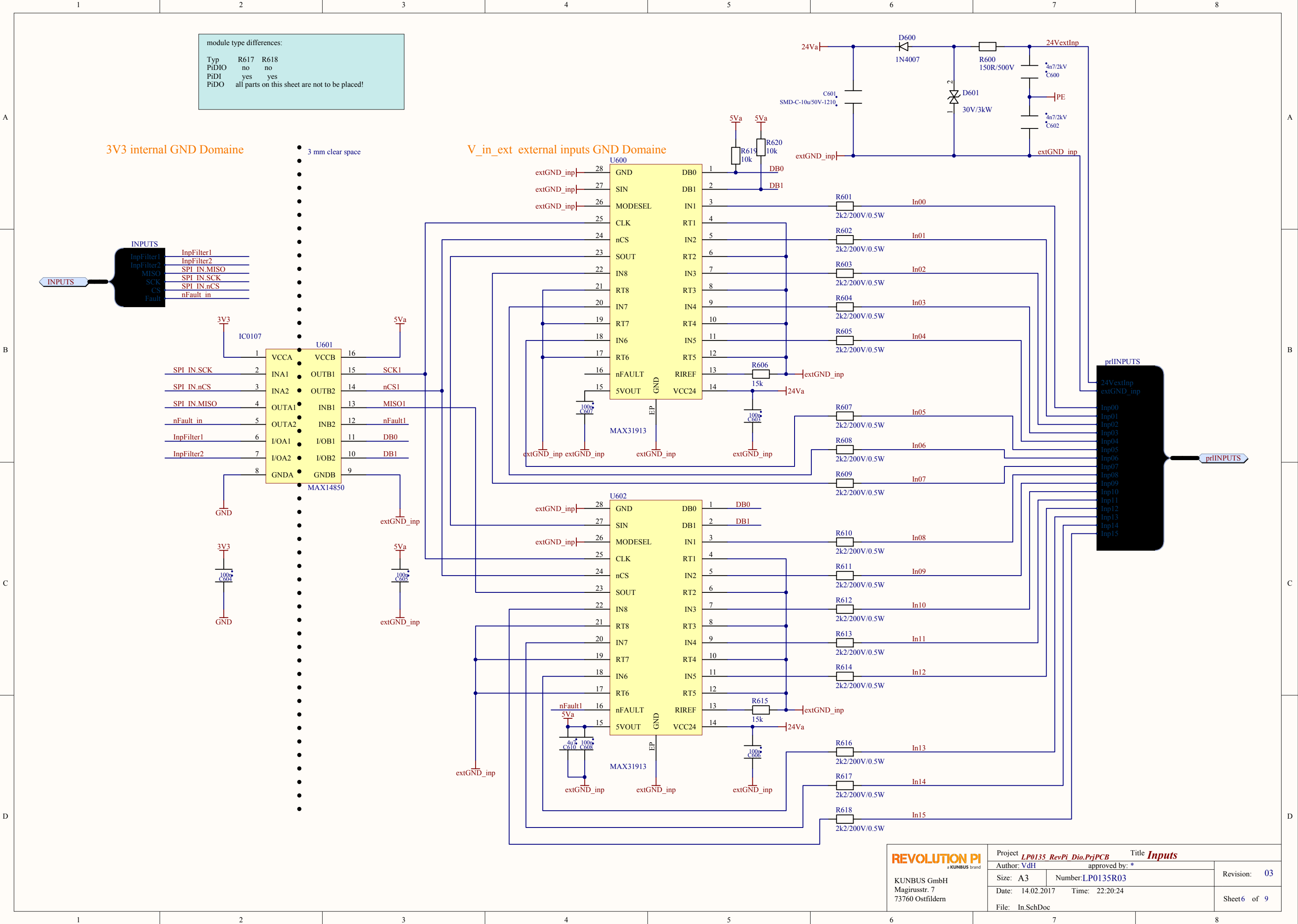
SPI Inputs

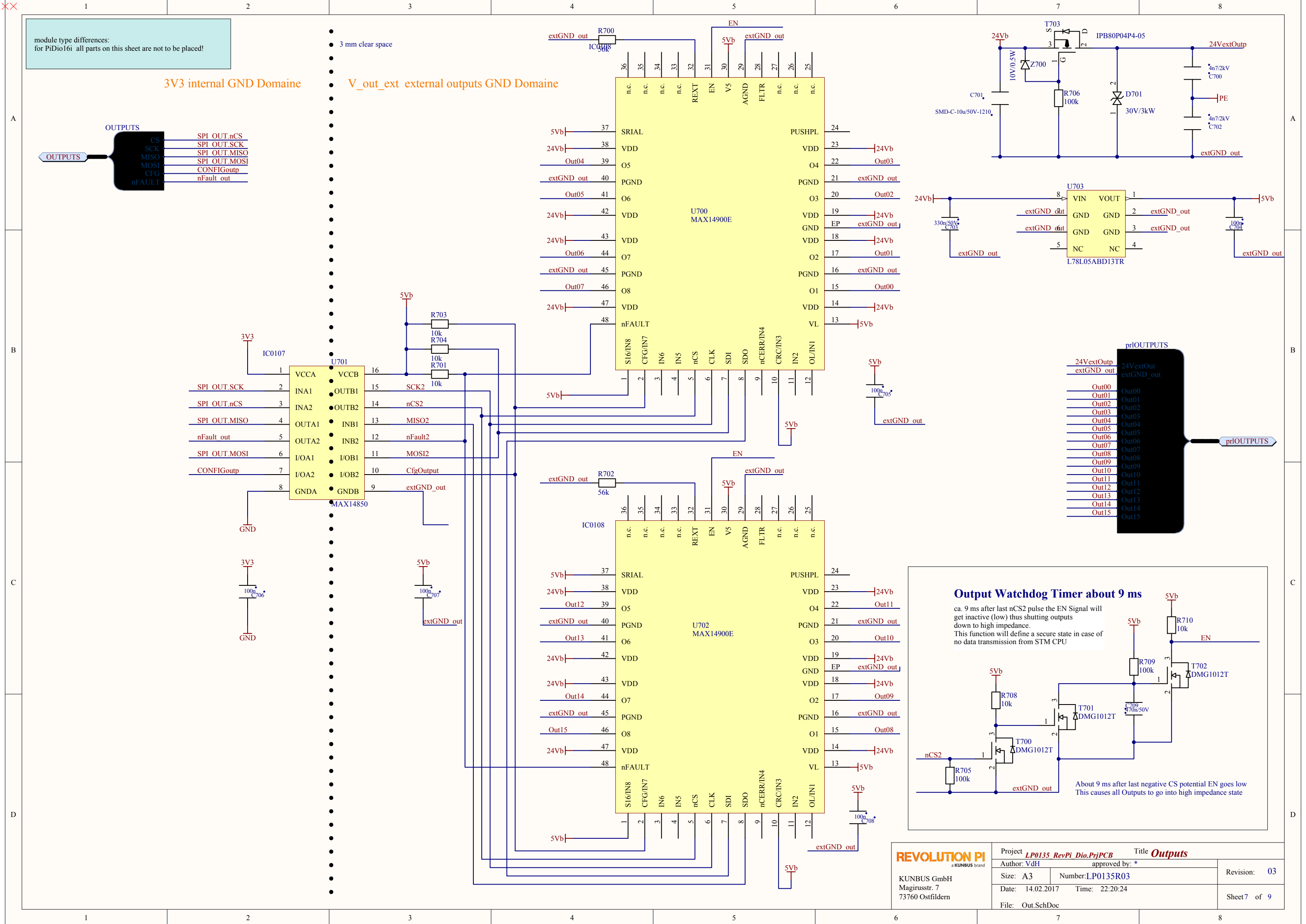


SPI Outputs



REVOLUTION PI a KUNBUS brand	Project LP0135_RevPi_Dio.PrjPCB Title µC	
	Author: VdH approved by: *	
	Size: A3	Number: LP0135R03
	Date: 14.02.2017	Time: 22:20:24
	File: uC.SchDoc	
KUNBUS GmbH Magirusstr. 7 73760 Ostfildern		Revision: 03 Sheet3 of 9





module type differences:
for PiDio16i all parts on this sheet are not to be placed!

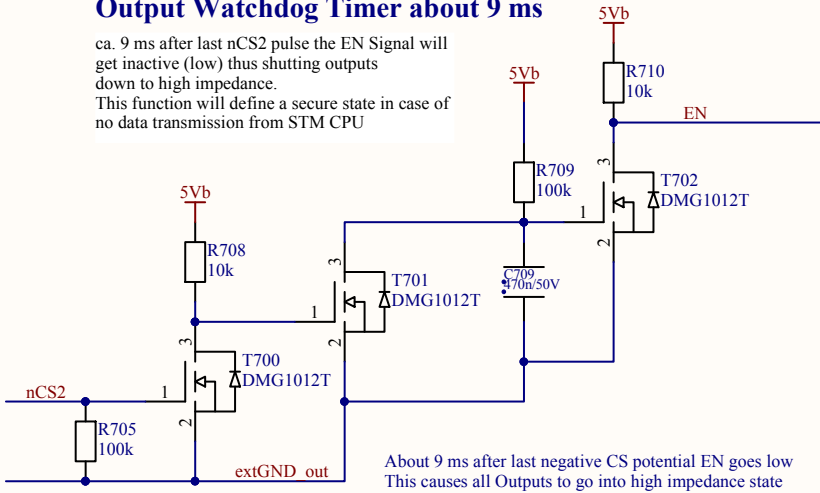
3V3 internal GND Domaine

V_out_ext external outputs GND Domaine

3 mm clear space

Output Watchdog Timer about 9 ms

ca. 9 ms after last nCS2 pulse the EN Signal will get inactive (low) thus shutting outputs down to high impedance.
This function will define a secure state in case of no data transmission from STM CPU



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Magirusstr. 7
73760 Ostfildern

Project LP0135 RevPi Dio.PrjPCB		Title Outputs	
Author: VdH		approved by: *	
Size: A3	Number: LP0135R03		Revision: 03
Date: 14.02.2017	Time: 22:20:24		Sheet 7 of 9
File: Out.SchDoc			

