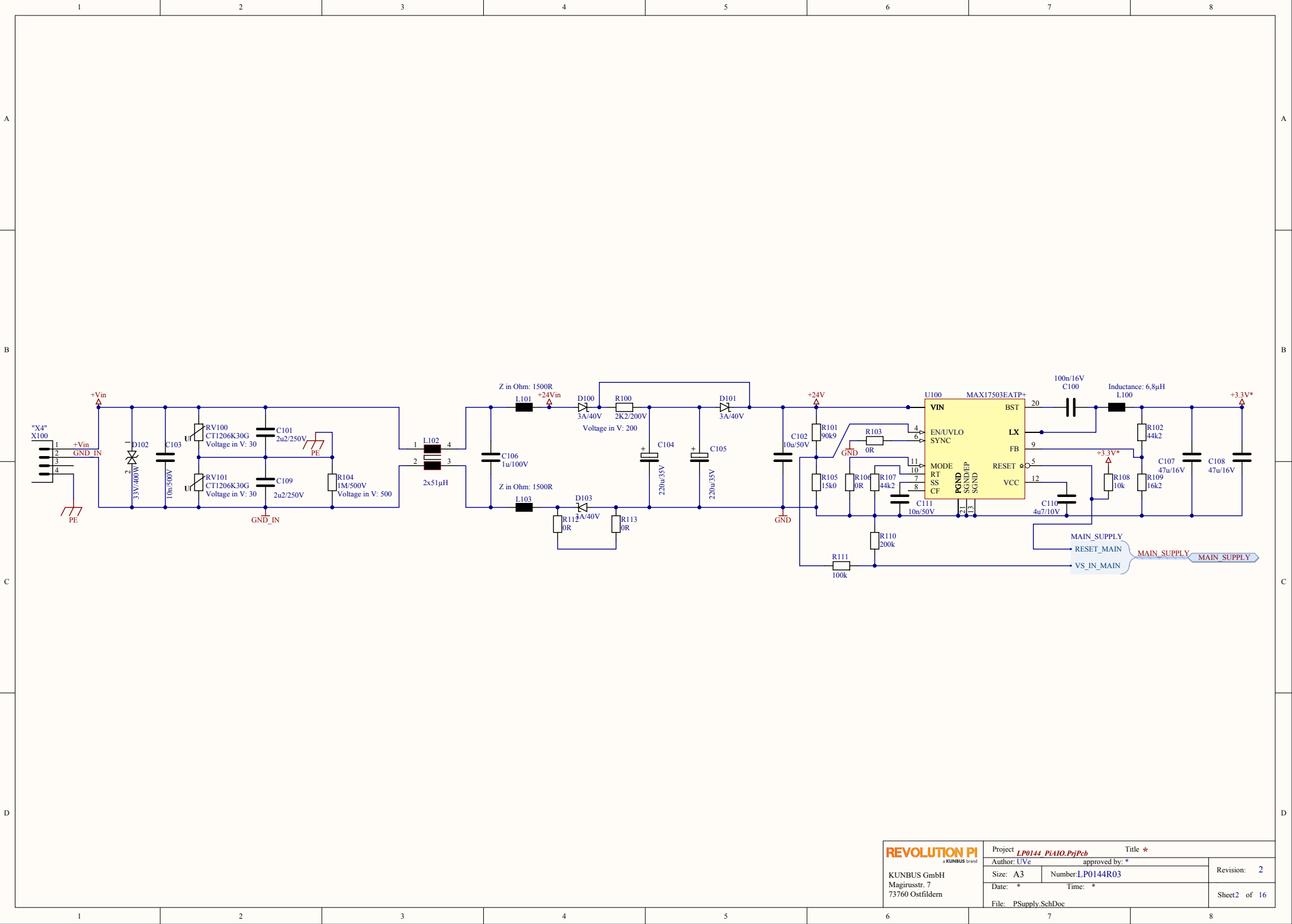
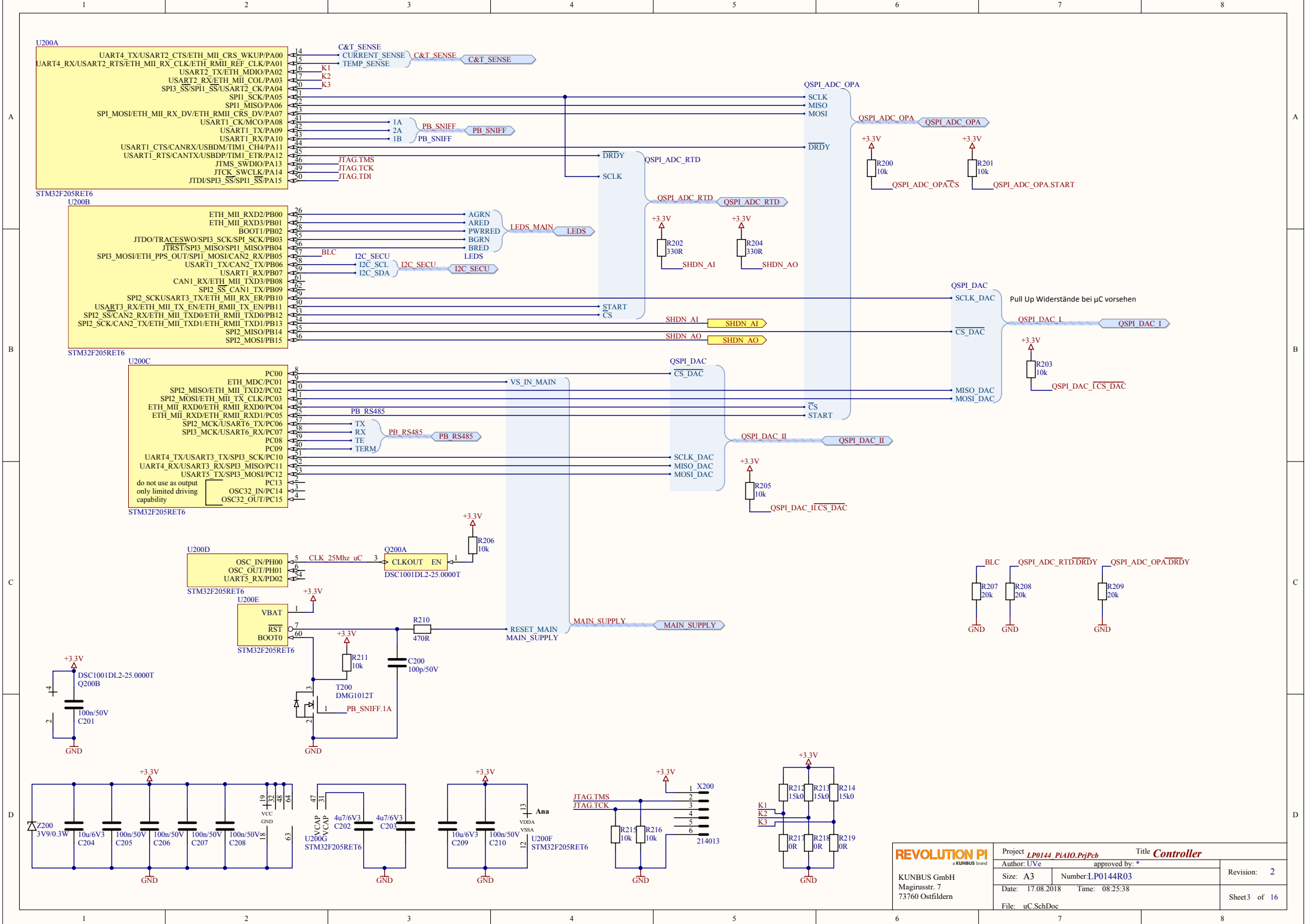


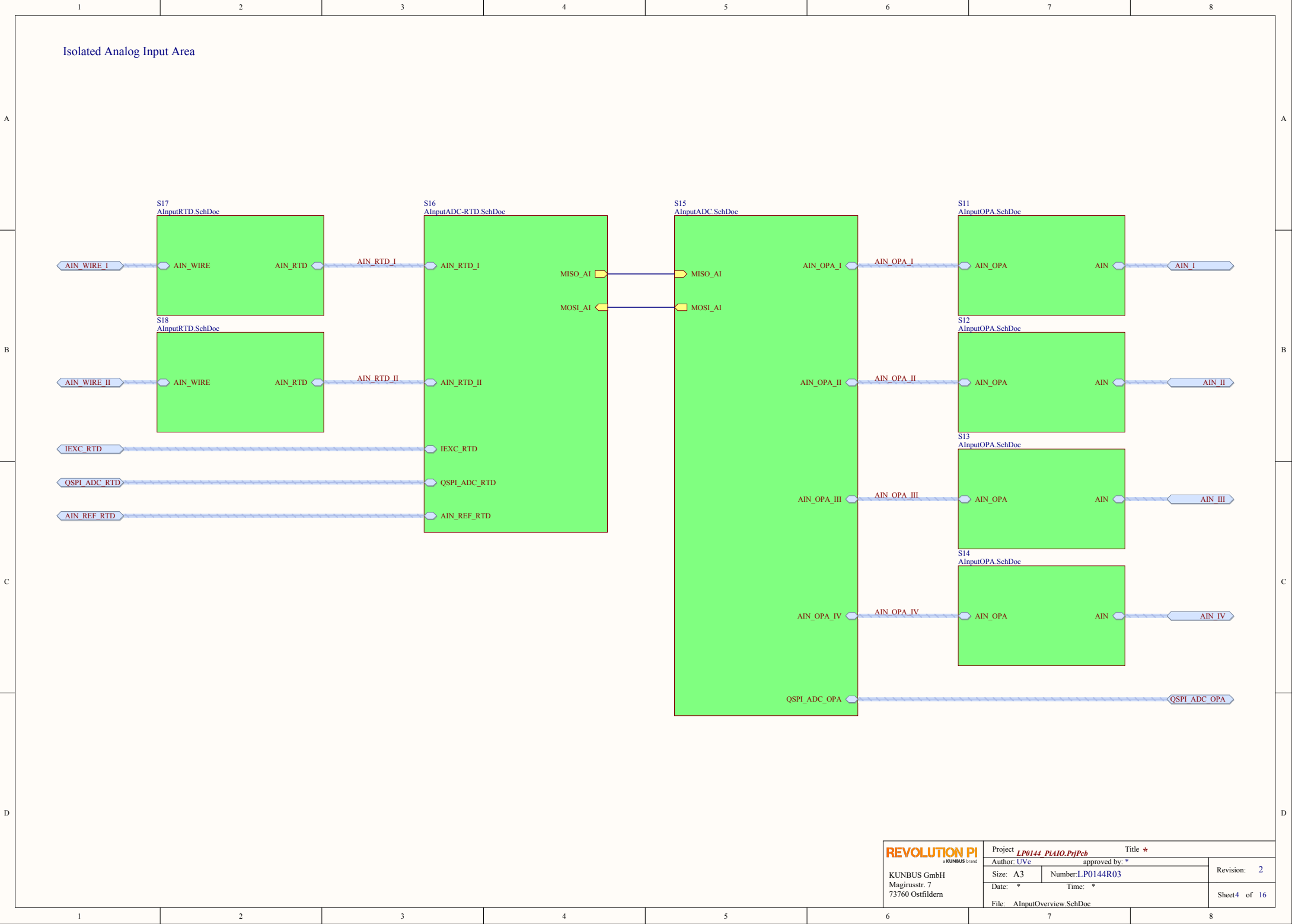
PiAIO





REVOLUTION PI a KUNBUS brand KUNBUS GmbH Magirusstr. 7 73760 Ostfildern	Project LP0144_PiAIO_PriPch Title *	
	Author: UVe	approved by: *
	Size: A3	Number: LP0144R03
	Date: *	Time: *
	File: PSupply.SchDoe	
Revision: 2		Sheet 2 of 16





Isolated Analog Input Area

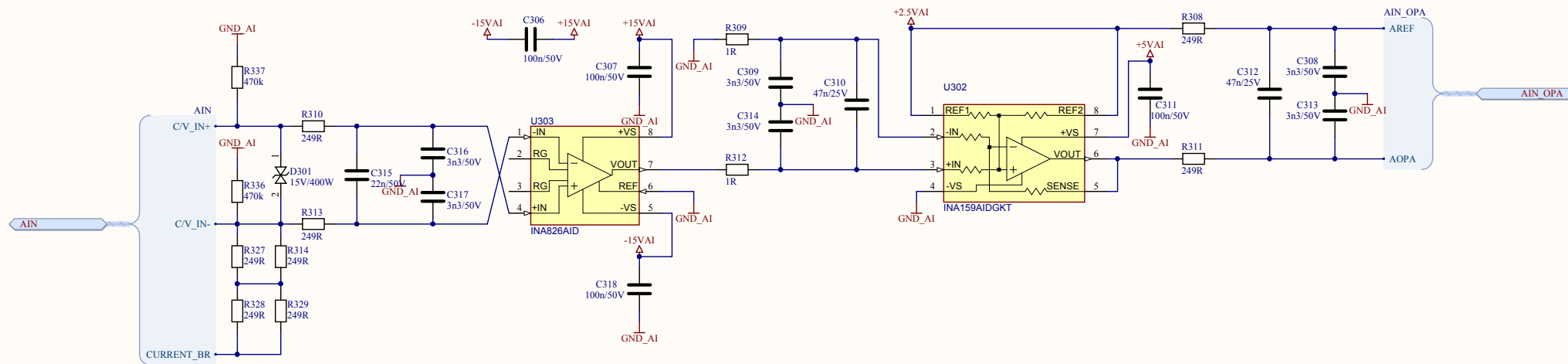


High Impedance Differential Amplifier

Isolated Analog Input Area

Gain Prescaler

Isolated Analog Input Area



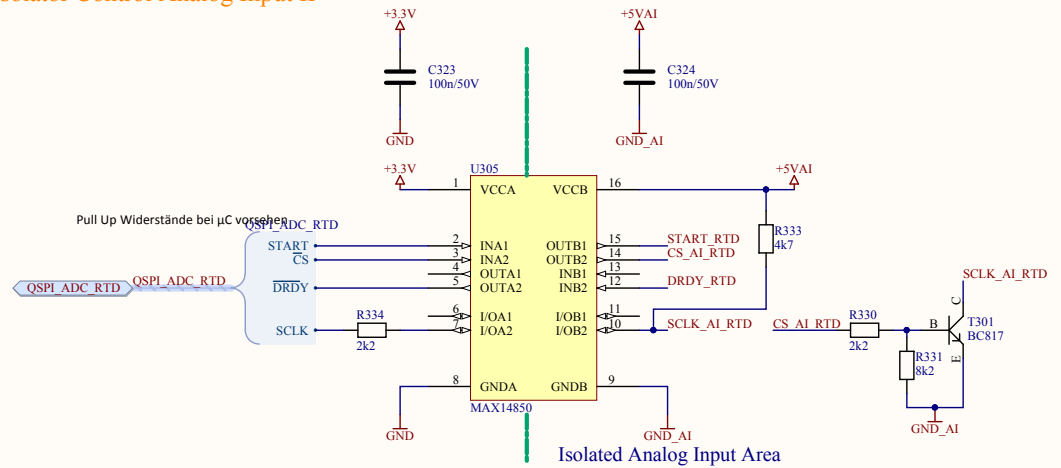
A

B

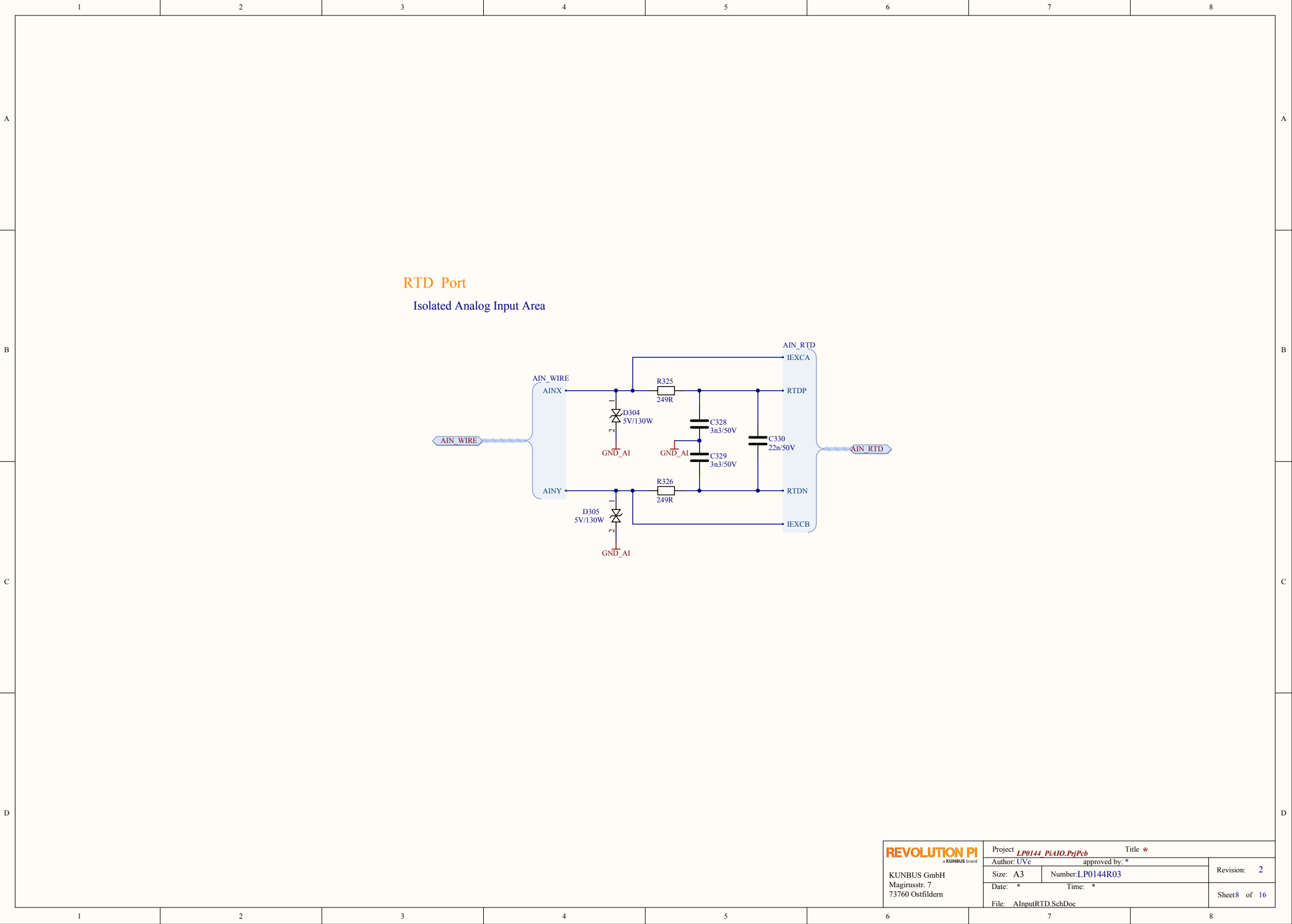


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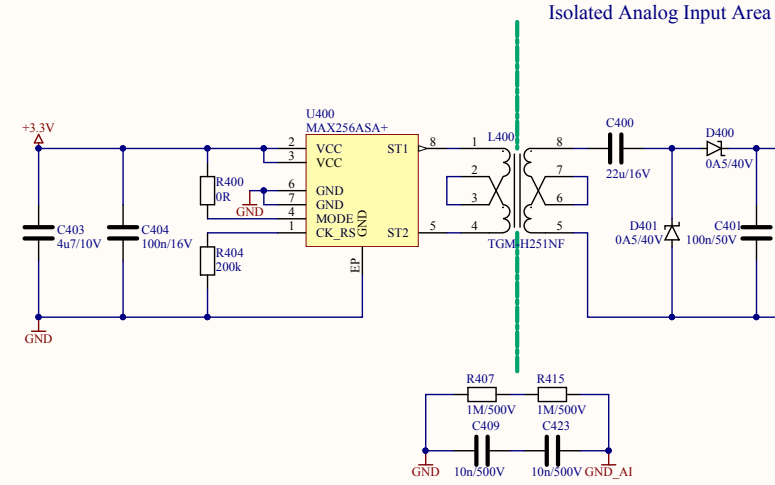
Pull Up Widerstände bei μC vorsehe



 KUNBUS GmbH Magirusstr. 7 73760 Ostfildern	Project <i>LP0144 PiAIO.PrjPcb</i> Title *	
	Author: <i>UVe</i> approved by: *	Revision: 2
	Size: A3 Number <i>L_P0144R03</i>	
	Date: * Time: *	Sheet 7 of 16
	File: AlnOutADC-RTD SchDoc	

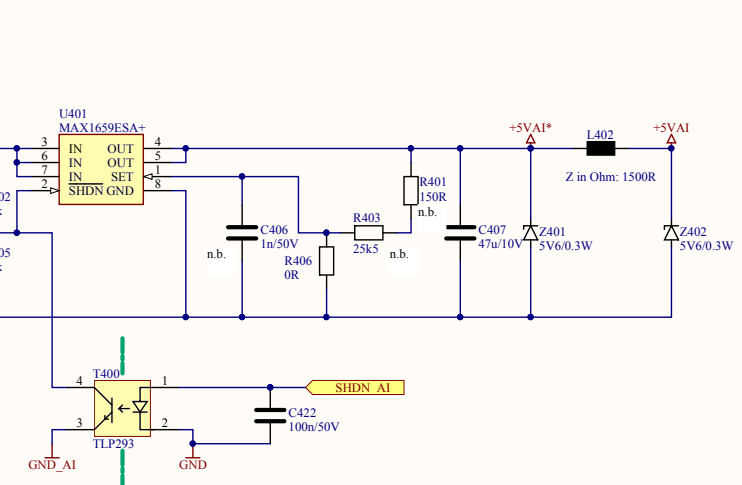


Step Up Converter +3.3V / Isolated +5V



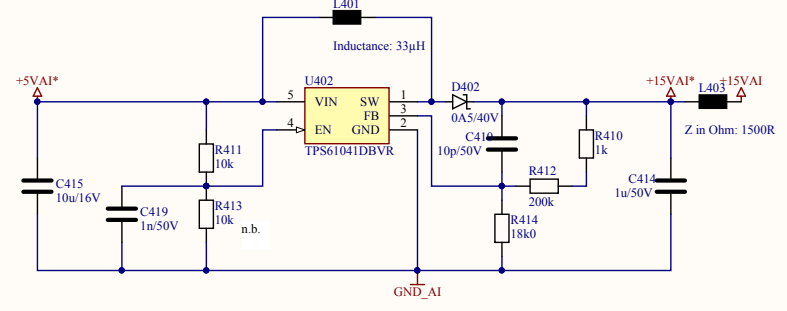
LDO AI

Isolated Analog Input Area



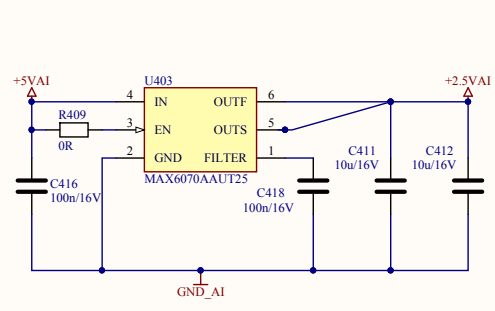
Step Up Converter +5V / +15V

Isolated Analog Input Area



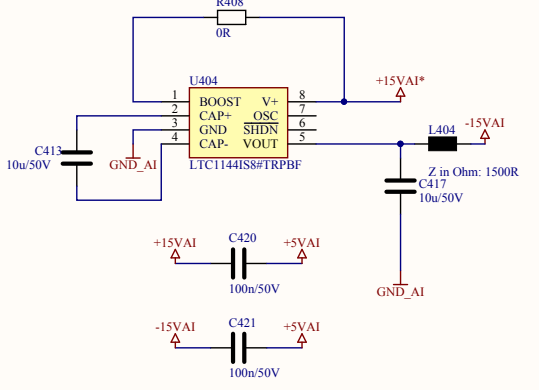
Voltage Reference +2.5V

Isolated Analog Input Area

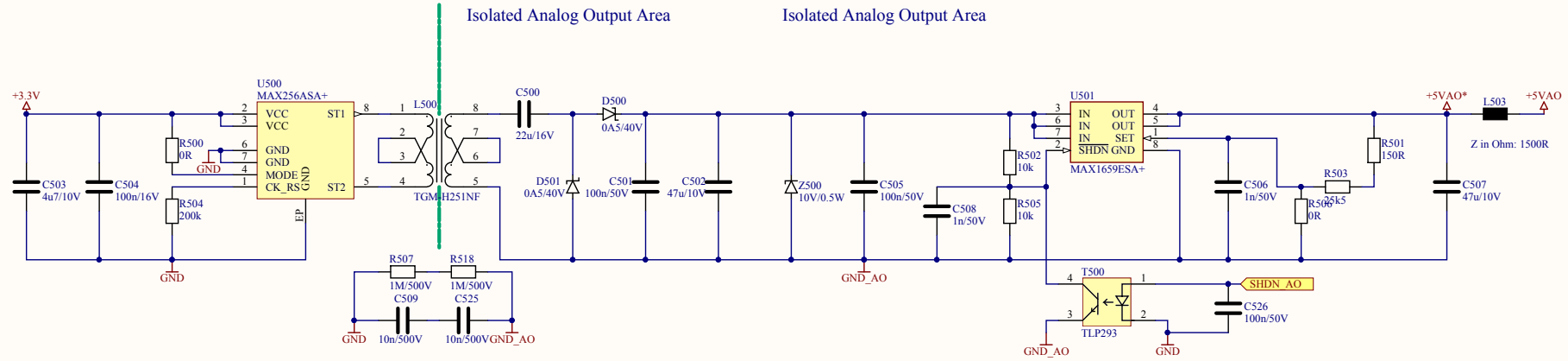


Inverter +15V / -15V

Isolated Analog Input Area



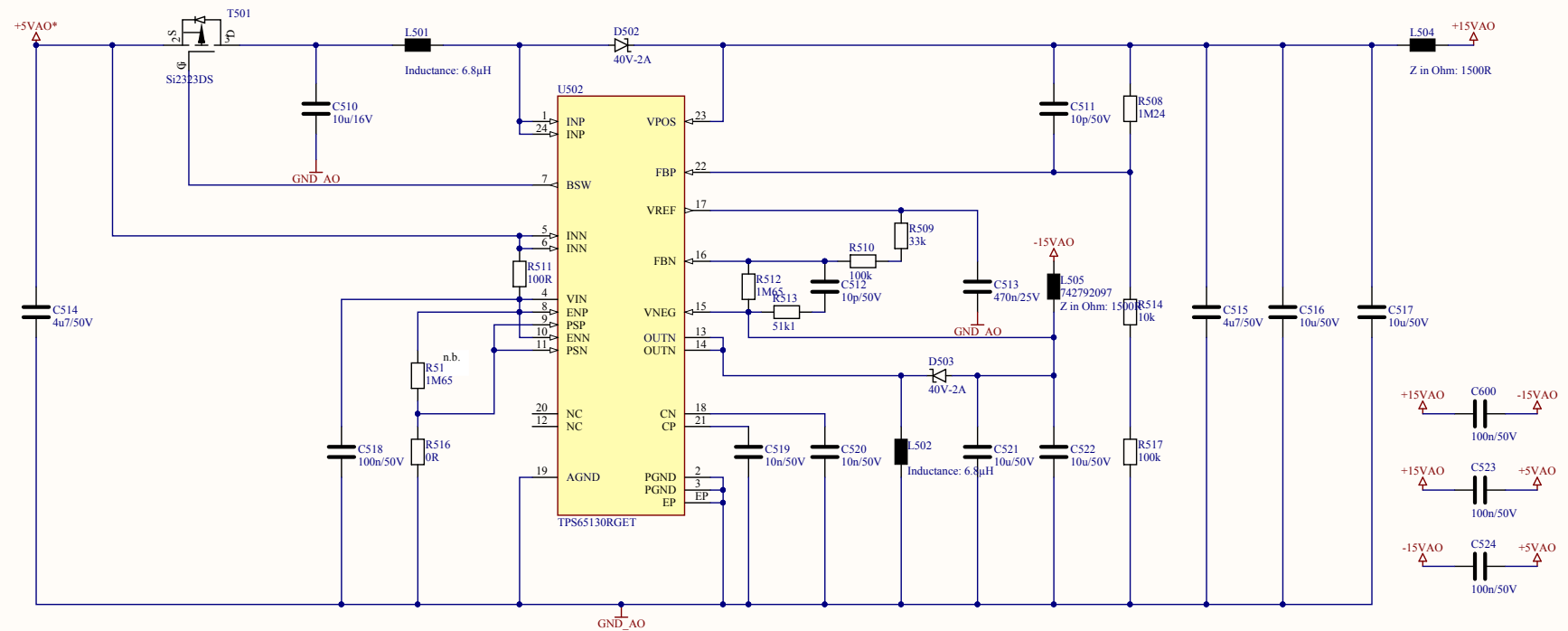
Step Up Converter +3.3V / Isolated +5V



LDO AO

Step Up Converter +5V / Dual +15V / -15V

Isolated Analog Output Area



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73760 Ostfildern

Project **LP0144_PiAIO_PriPcb**

Author: **UVe**

Size: **A3**

Date: *****

File: **AOutputPSupply.SchDoc**

Title *****

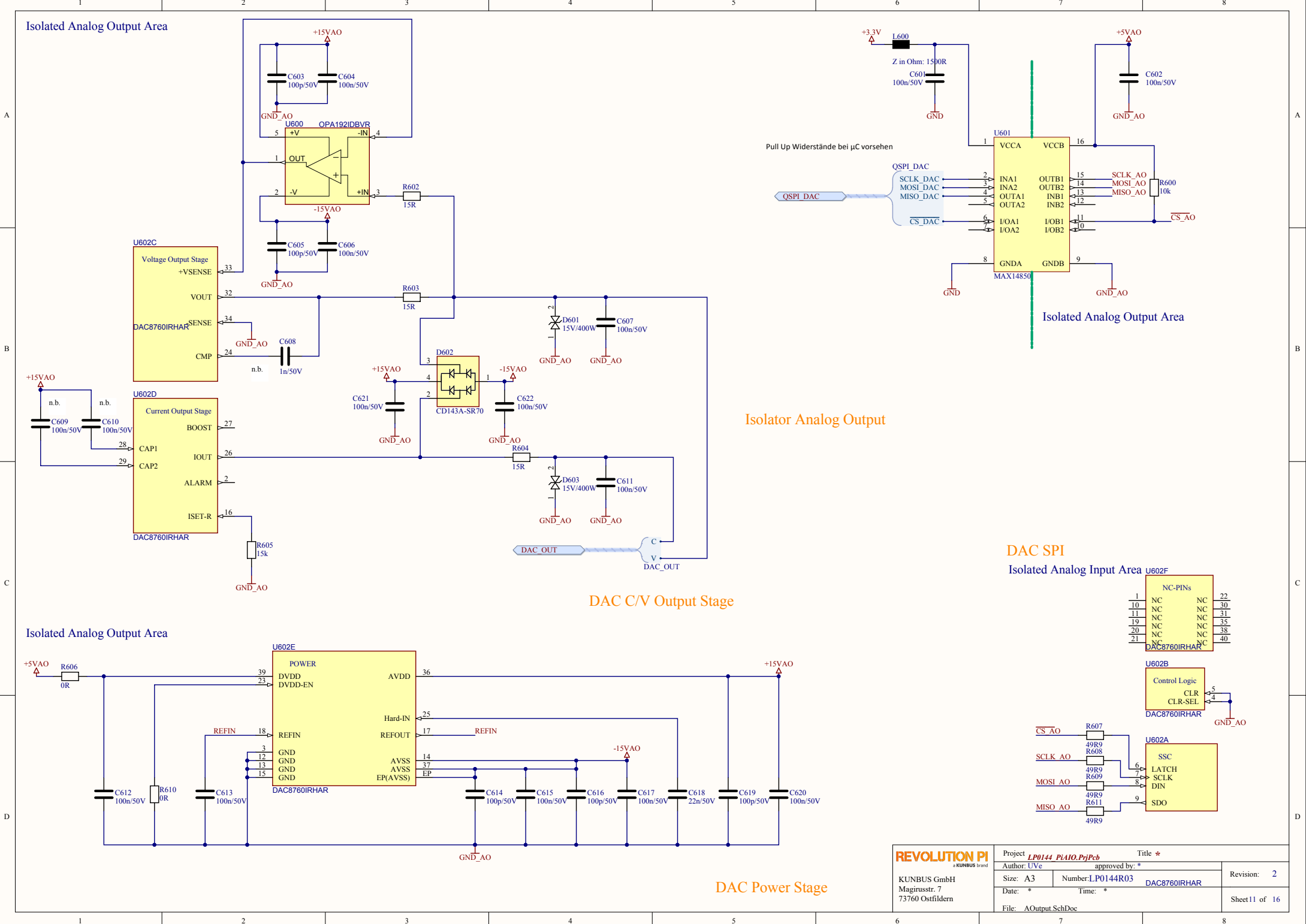
approved by: *****

Number: **LP0144R03**

Time: *****

Revision: **2**

Sheet 10 of 16



Isolated Analog Output Area

Isolated Analog Output Area

Isolator Analog Output

DAC C/V Output Stage

Isolated Analog Output Area

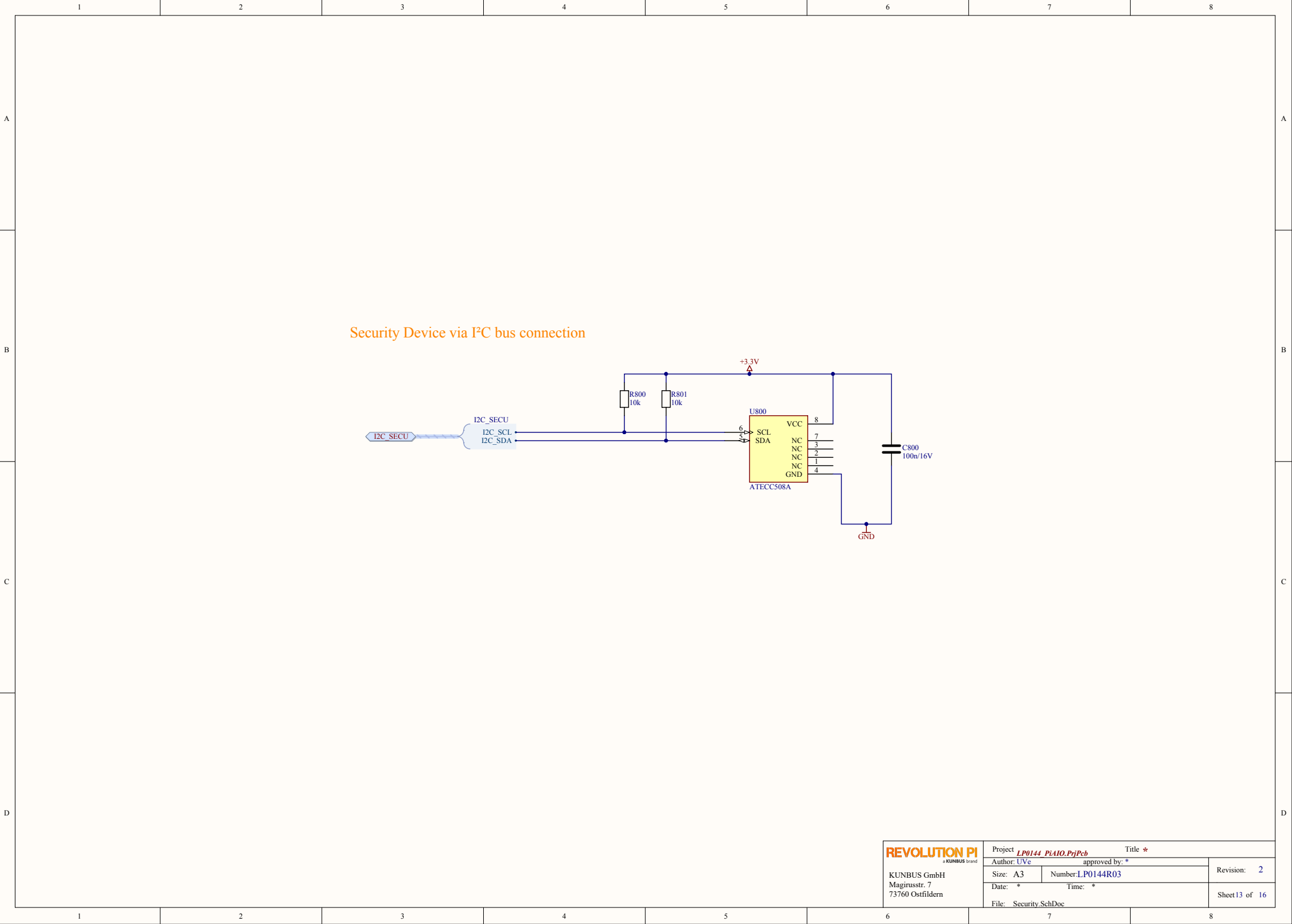
DAC Power Stage

DAC SPI

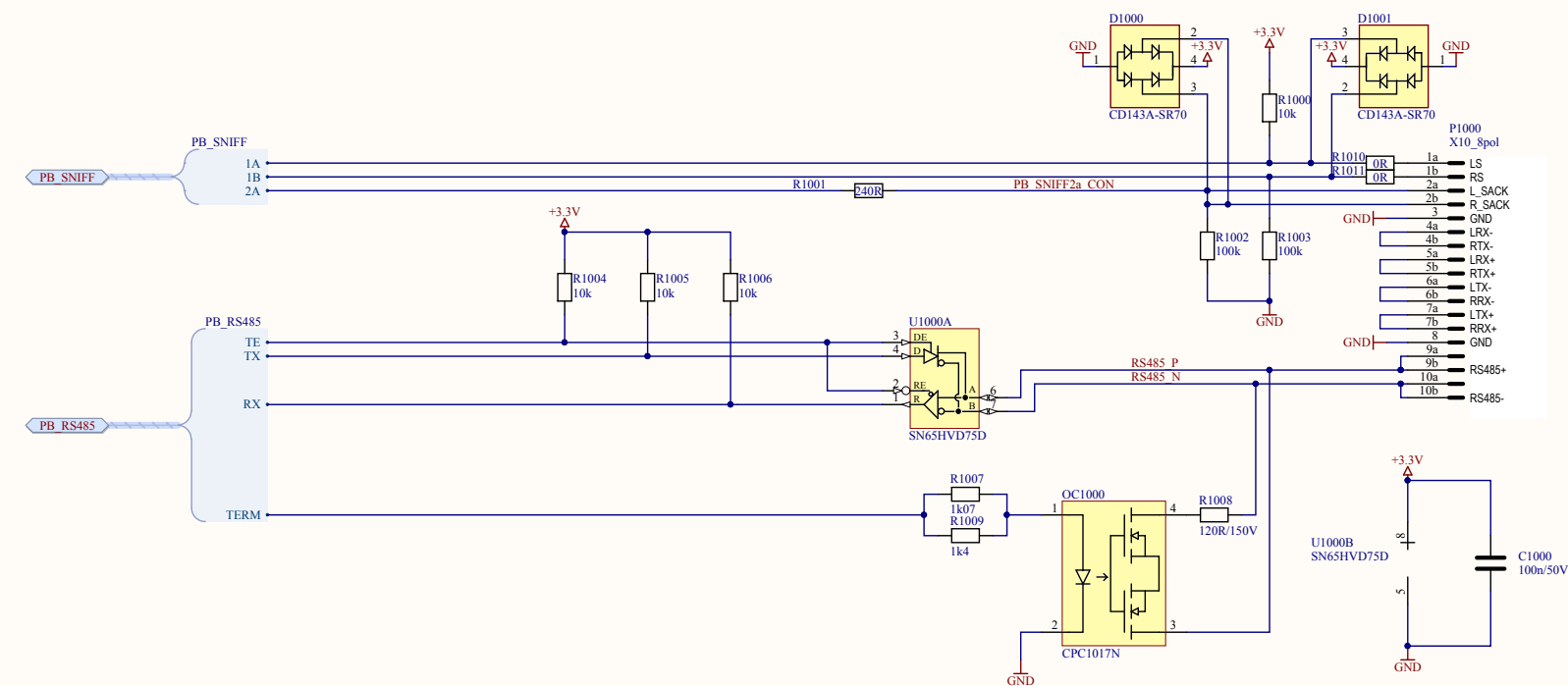
Isolated Analog Input Area

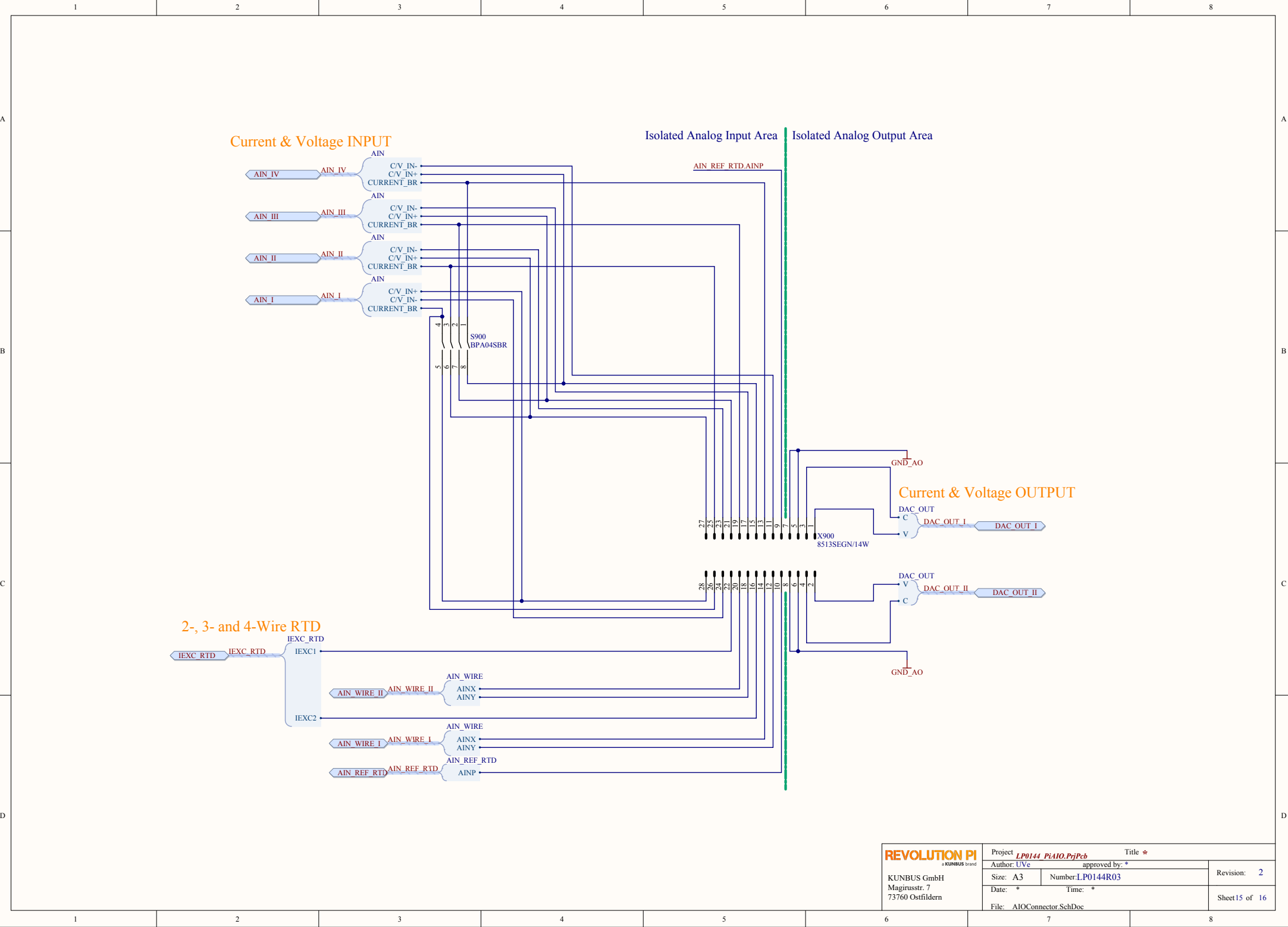
POWER

-
- The schematic diagram illustrates the LED driver circuit. It features a PWRRED input line connected to a MOSFET (T1701 DMG1012T) through a 10k resistor (R704). A 1k resistor (R705) is connected between the PWRRED line and ground. A 100pF/50V capacitor (C70) is connected between the MOSFET gate and ground. The MOSFET is connected to the AGRN, ARED, BGRN, and BRED lines. The LEDs (LD701A, LD701B, LD702A, LD702B) are connected to ground through 1k resistors (R706, R707, R708, R709). The LEDs are connected to the AGRN, ARED, BGRN, and BRED lines. The LEDs are connected to ground through 1k resistors (R706, R707, R708, R709).



RS485 & Programing Interface





Current & Temperature Sens

